

Extreme DA and UMC Collaborate to Provide Sub 65-Nanometer Variation-Aware IC Design Flows

DESIGN AUTOMATION CONFERENCE, Anaheim, Calif.— June 9, 2008 — Extreme DA™, a leading provider of new-generation integrated circuit (IC) performance and yield improvement solutions, and UMC (NYSE: UMC, TSE: 2303), a leading global semiconductor foundry, announced their collaboration on variation-aware IC design flows for 65nm and finer process technologies. The flows reduce design uncertainty and predict design performance and yield, by analyzing timing behavior in the presence of process variations. The first flow, based on the Extreme Gold™ statistical analysis suite, has already been applied to a test-chip at UMC's 65nm process node. This collaboration builds on previous cooperation for the 90nm node.

The current 65nm partnership includes collaborative efforts in the following areas:

- Characterization of UMC libraries for global and mismatch variations to analyze these effects
- Location-based on-chip variation (LOCV) model construction for improved accuracy of variation analysis
- Variation-aware extraction and timing analysis on a 65nm test chip.

Mutual customers of UMC and Extreme DA will experience improved parametric yield, and reduced pessimism, and optimized performance for ICs produced in UMC's proven 65nm process through the capabilities of the Extreme Gold suite.

"UMC is continually striving to improve parametric yield prediction and performance optimization capabilities for our customers," said Darsun Tsien, UMC's vice president of design methodology. "The Extreme Gold statistical analysis suite has demonstrated its capability in the timing analysis and modeling of on-chip process variations. We look forward to offering the benefits of these new design flows to our 65nm customers."

Extreme DA CEO, Mustafa Celik, said, "We are honored that UMC, one of the world's top foundries, is continuing to develop these unique, variation-aware design and timing flows for advanced systems-on-chips (SoCs). We expect that they will satisfy the fabless

semiconductor companies' demands for faster, more accurate timing closure and higher chip yields."

About The Extreme Gold Statistical Suite

The Extreme Gold sign-off suite is a variation-aware performance and yield analysis flow for 65 and 45nm ICs. The suite models IC variations and analyzes the statistical effects on extraction, timing, and noise using the GoldTime™ timing analyzer. Optimization of design performance and yield by the suite ensures successful ICs.

About UMC

UMC (NYSE: UMC, TSE: 2303) is a leading global semiconductor foundry that manufactures advanced system-on-chip (SoC) designs for applications spanning every major sector of the IC industry. UMC's SoC Solution Foundry strategy is based on the strength of the company's advanced technologies, which include production proven 90nm, 65nm, mixed signal/RFCMOS, and a wide range of specialty technologies. Production is supported through 10 wafer manufacturing facilities that include two advanced 300mm fabs; Fab 12A in Taiwan and Singapore-based Fab 12i are both in volume production for a variety of customer products. The company employs approximately 13,000 people worldwide and has offices in Taiwan, Japan, Singapore, Europe, and the United States. UMC can be found on the web at <http://www.umc.com>.

About Extreme DA

Headquartered in Palo Alto, Calif., venture-funded Extreme DA develops and licenses software products to improve the performance and yield of nanometer integrated circuits. The company's investors include Foundation Capital, IT-Farm Corporation, and Lanza techVentures. For the latest news and information on Extreme DA, visit www.extreme-da.com or write to info@extreme-da.com. **Visit Extreme DA in booth 1364 at DAC in Anaheim, Calif. June 9-12, 2008**

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